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Docket JP920010326US1

SEP 29 2006

Appl. No.: 10/736,343

Filed: December 15, 2003

In the United States Patent and Trademark OfficeIn re the application of:
Rajendra K. Bera

Filed: 12/15/2003

Group Art Unit: 2183

For: Run-Time Parallelization
of Loops in Computer
Programs

Examiner: Ryan Paul Fiegle

Appl. No.: 10/736,343

Applicant's Docket:
JP920010326US1**CERTIFICATE OF FACSIMILE TRANSMISSION**I hereby certify that this correspondence is being transmitted via facsimile
to the United States Patent Office on the date indicated by my signature below.*Anthony V.S. England*
Anthony V.S. England*9-29-2006*
Date**AMENDMENT AFTER REPLY A**The following amendment is responsive to discussions with Examiner Fiegle after the
filing of Applicant's Reply A.